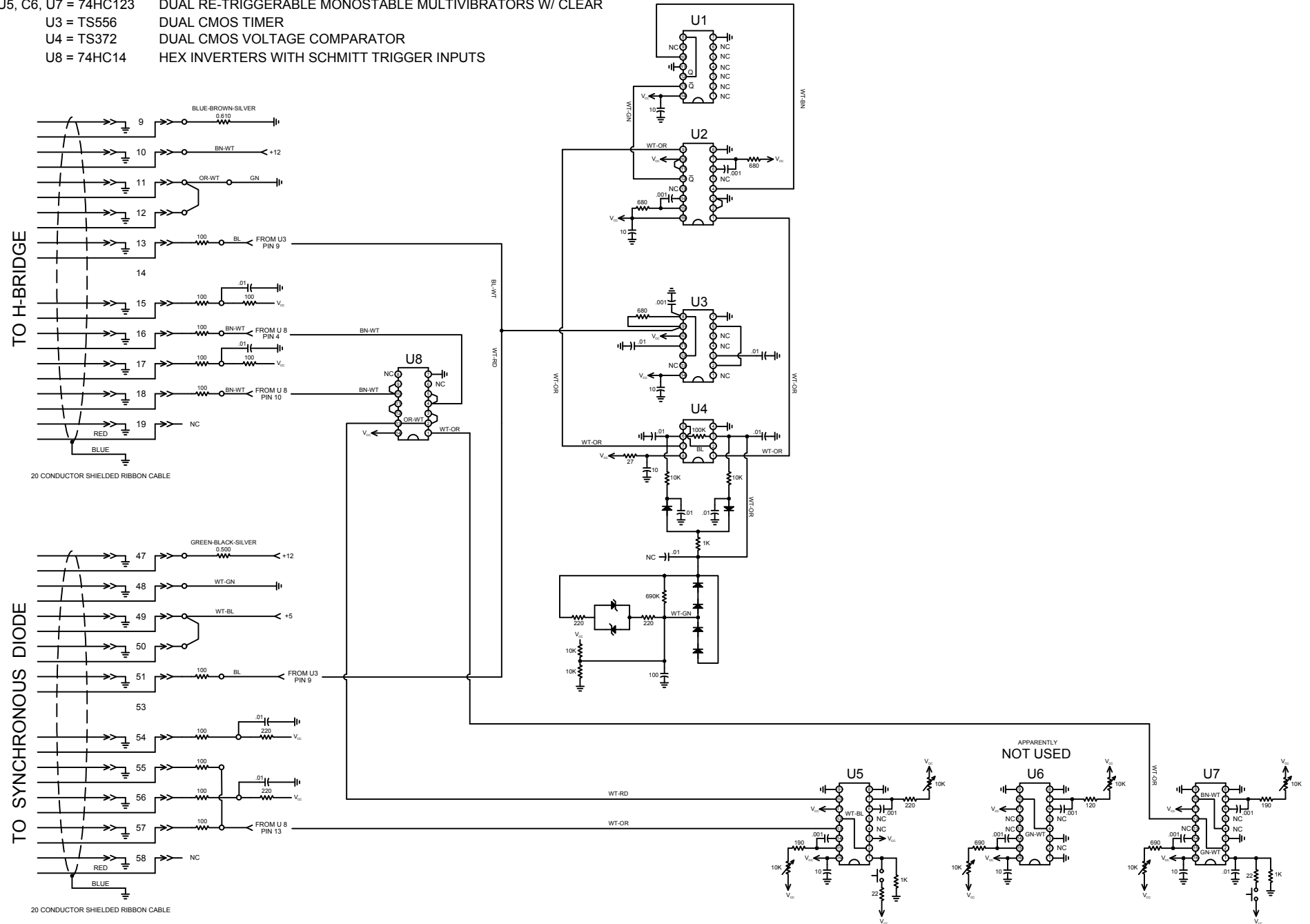


U1 = 74HC74  
 U2, U5, C6, U7 = 74HC123  
 U3 = TS555  
 U4 = TS372  
 U8 = 74HC14

DUAL D-TYPE POSITIVE EDGE TRIGGERED FLIP-FLOP W/ PRESET & CLEAR  
 DUAL RE-TRIGGERABLE MONOSTABLE MULTIVIBRATORS W/ CLEAR  
 DUAL CMOS TIMER  
 DUAL CMOS VOLTAGE COMPARATOR  
 HEX INVERTERS WITH SCHMITT TRIGGER INPUTS



Graham Gunderson Logic Controller System Wiring Diagram  
 Presentation Version drafted 7/28/2016 by Mark McKay